

ABSTRACT OF THE DISCLOSURE

An image signal resulting from an image-capturing operation performed at a CCD undergoes analog processing at an image processing unit. Image data resulting from the analog processing are first digitized at an A/D conversion circuit and then undergo image processing at a DSP. The image data having undergone the image processing are stored in a frame memory. At a peaking processing circuit, a brightness signal Y stored in a Y signal memory of the frame memory is read out and differentiated at a differentiation circuit. The differentiated signal is amplified at an amplifier circuit and then the amplified signal is added to the original brightness signal Y at an adder circuit. A brightness signal Y' representing the sum is output to a display LCD. The edges in the image signal are emphasized in the display on the display LCD.